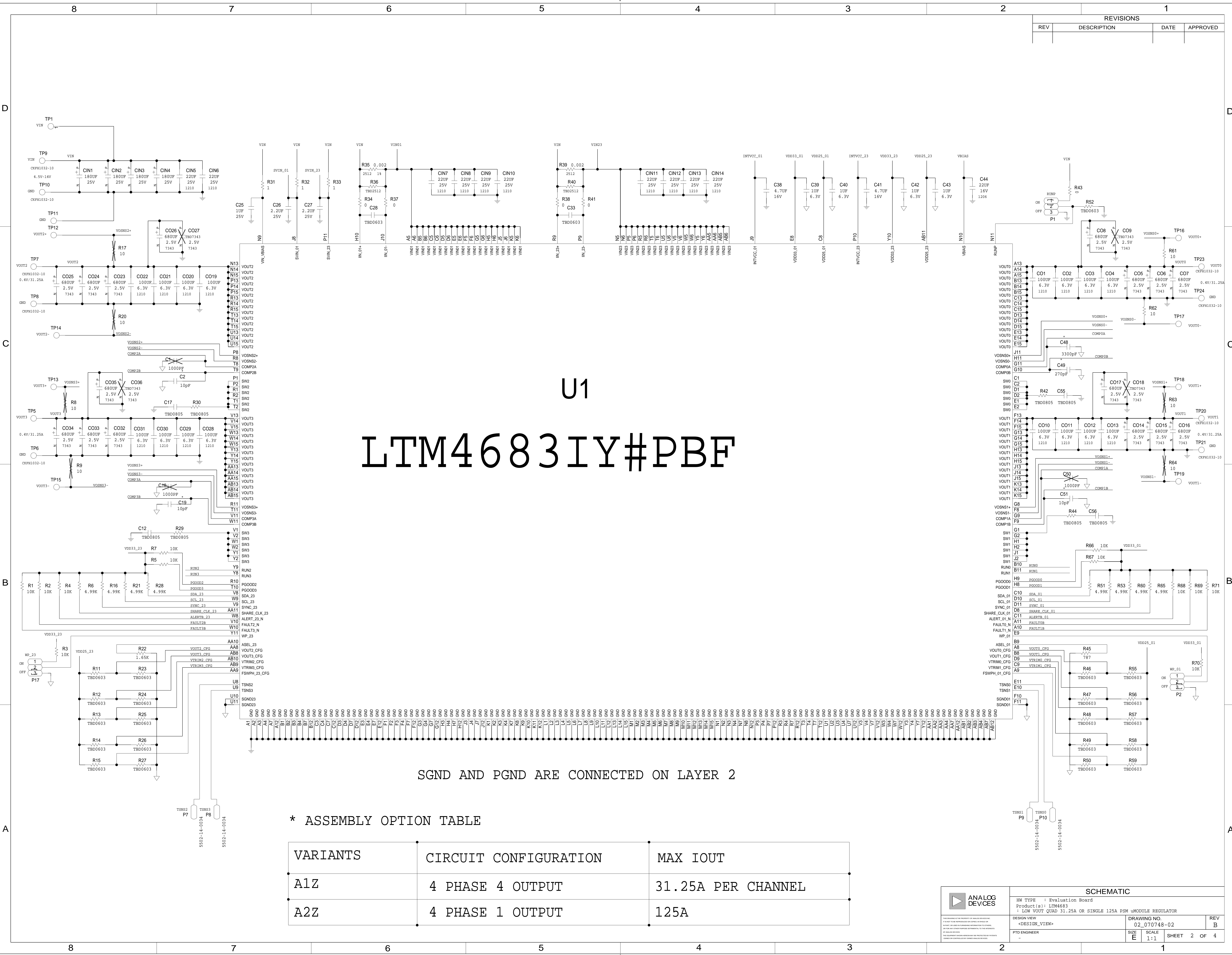



8		7		6		5		4		3		2		1																											
D	THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.				<div>JUMPER TABLE</div> <table><tr><td>JP#</td><td>ON</td><td>OFF</td></tr><tr><td>1</td><td></td><td></td></tr><tr><td>2</td><td></td><td></td></tr><tr><td>3</td><td></td><td></td></tr><tr><td>4</td><td></td><td></td></tr><tr><td>5</td><td></td><td></td></tr></table> <div>* SEE ASSEMBLY INSTRUCTIONS</div>			JP#	ON	OFF	1			2			3			4			5			<div>REVISIONS</div> <table><tr><td>REV</td><td>DESCRIPTION</td><td>DATE</td><td>APPROVED</td></tr><tr><td>B</td><td>PRODUCTION PCB</td><td>8/1/23</td><td>GL</td></tr></table>								REV	DESCRIPTION	DATE	APPROVED	B	PRODUCTION PCB	8/1/23	GL
								JP#	ON	OFF																															
								1																																	
2																																									
3																																									
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RELAY CONTROL CHART																																									
CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR																																					
C																																									
B																																									
A																																									
P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER																																					

TEMPLATE ENGINEER -	DATE 18AUG20	SCHEMATIC						
HARDWARE SERVICES -	18AUG20							
HARDWARE SYSTEMS -	18AUG20	HW TYPE : Evaluation Board Product(s): LTM4683 : LOW VOUT QUAD 31.25A OR SINGLE 125A PSM uMODULE REGULATOR PACKAGE : LeadCount-lead BodySize PackageType-family : Pitch-pitch StyleVendor Style						
TEST ENGINEER -								
COMPONENT ENGINEER -	18AUG20							
TEST PROCESS -								
HARDWARE RELEASE -	DDMMYY	<User Define> <User Define> <User Define>						
DESIGNER -	DDMMYY	MASTER PROJECT TEMPLATE TBD	TESTER TEMPLATE 065804_a	DRAWING NO. 02_070748-02		REV. B		
PTD ENGINEER -	DDMMYY	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES						
CHECKER -	DDMMYY	DECIMALS X.XX +0.010 X.XXX +0.005	FRACTIONS +1/32	ANGLES +2	SIZE D	SCALE 1:1	CODE ID NO. CodeID	SHEET 1 of 4



\* ASSEMBLY OPTION TABLE

VARIANTS	CIRCUIT CONFIGURATION	MAX IOUT
A1Z	4 PHASE 4 OUTPUT	31.25A PER CHANNEL
A2Z	4 PHASE 1 OUTPUT	125A



HW TYPE : Evaluation Board  
Product(s) : LTM4683  
: LOW VOUT QUAD 31.25A OR SINGLE 125A PSM uMODULE REGULATOR

DESIGN VIEW  
<DESIGN\_VIEW>

PTD ENGINEER  
-

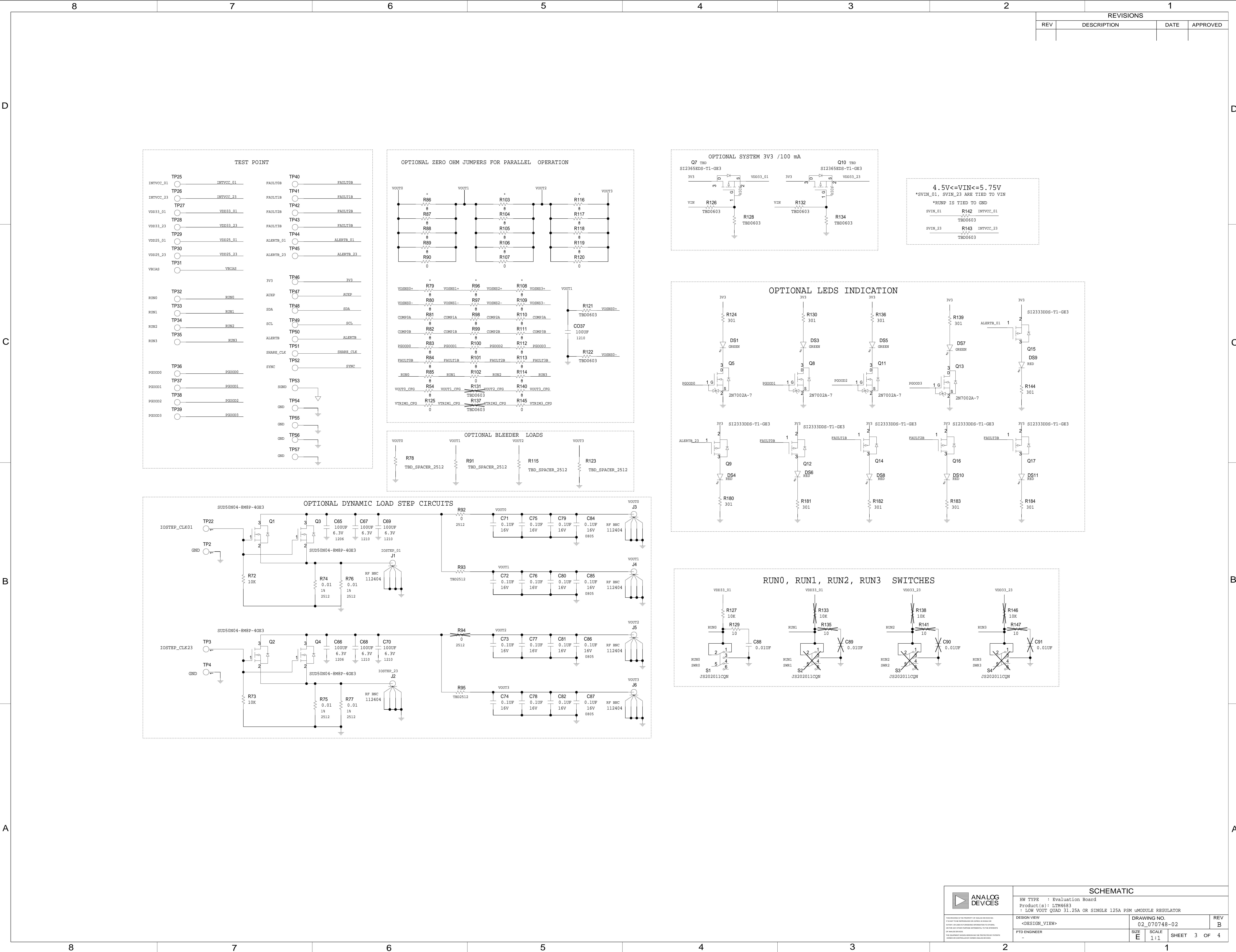
DRAWING NO.  
02\_070748-02

REV  
B

SIZE  
E

SCALE  
1:1

SHEET  
2 OF 4



OPTIONAL DYNAMIC LOAD STEP CIRCUITS

TP22

TP2

TP3

TP4

TP22

TP2

TP3

TP4

TP22

TP2

TP3

TP4

TP22

TP2

TP3

TP4

OPTIONAL SYSTEM 3V3 /100 mA

Q7

Q10

R126

R132

R128

R134

TBD0603

TBD0603

TBD0603

TBD0603

4.5V<=VIN<=5.75V

\*SVIN\_01, SVIN\_23 ARE TIED TO VIN

\*RUNP IS TIED TO GND

SVIN\_01

R142

INTVCC\_01

TBD0603

SVIN\_23

R143

INTVCC\_23

TBD0603

OPTIONAL LEDS INDICATION

3V3

DS1

DS3

DS5

DS7

Q5

Q8

Q11

Q13

Q15

Q9

Q12

Q14

Q16

Q17

DS4

DS6

DS8

DS10

DS11

R124

R130

R136

R139

R144

R180

R181

R182

R183

R184

SI2333DDS-T1-GE3

SI2333DDS-T1-GE3

SI2333DDS-T1-GE3

SI2333DDS-T1-GE3

SI2333DDS-T1-GE3

RUN0, RUN1, RUN2, RUN3 SWITCHES

VDD33\_01

VDD33\_01

VDD33\_23

VDD33\_23

R127

R129

R133

R135

R138

R141

R146

R147

C88

C89

C90

C91

S1

S2

S3

S4

JS202011CQN

JS202011CQN

JS202011CQN

JS202011CQN

ANALOG DEVICES

HW TYPE : Evaluation Board

Product(s): LTM4683

: LOW VOUT QUAD 31.25A OR SINGLE 125A PSM uMODULE REGULATOR

DESIGN VIEW

<DESIGN\_VIEW>

PTD ENGINEER

-

DRAWING NO.

02\_070748-02

REV

B

SIZE

1:1

SHEET

3

OF

4

